

Fig. 1

3: CONTROL CIRCUIT  
 4: DRIVING SIGNAL GENERATING CIRCUIT  
 15: CONTROL CIRCUIT

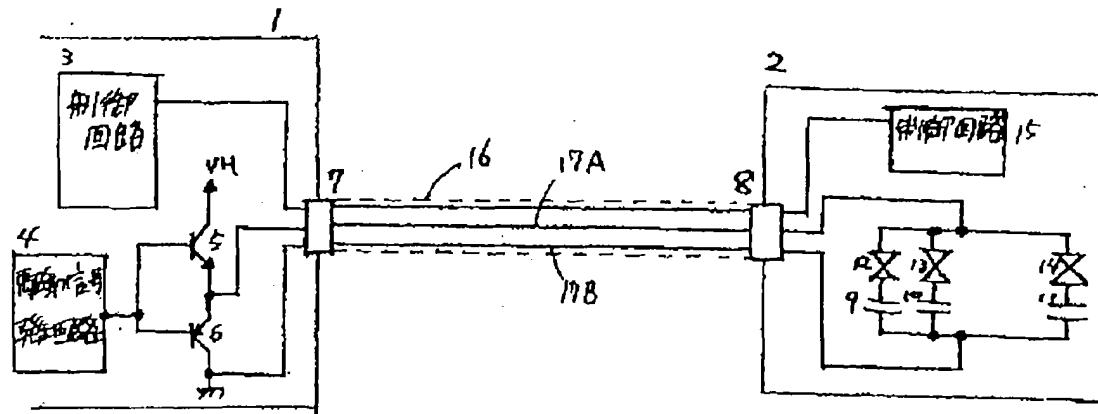


Fig. 2

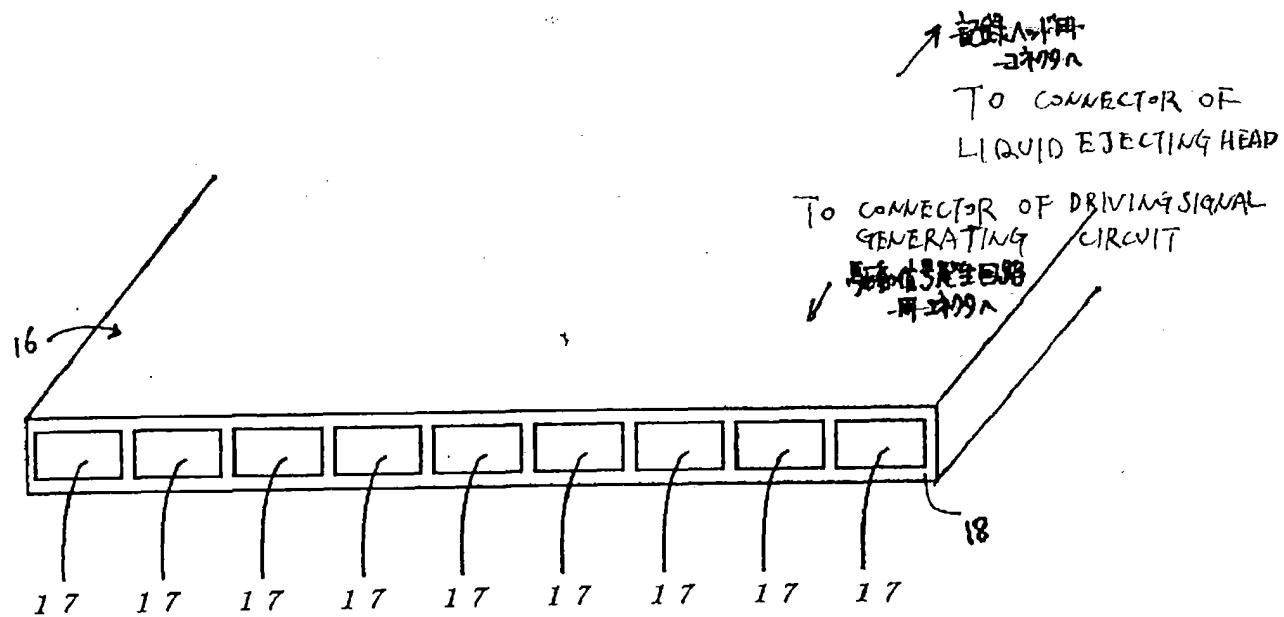


Fig. 3

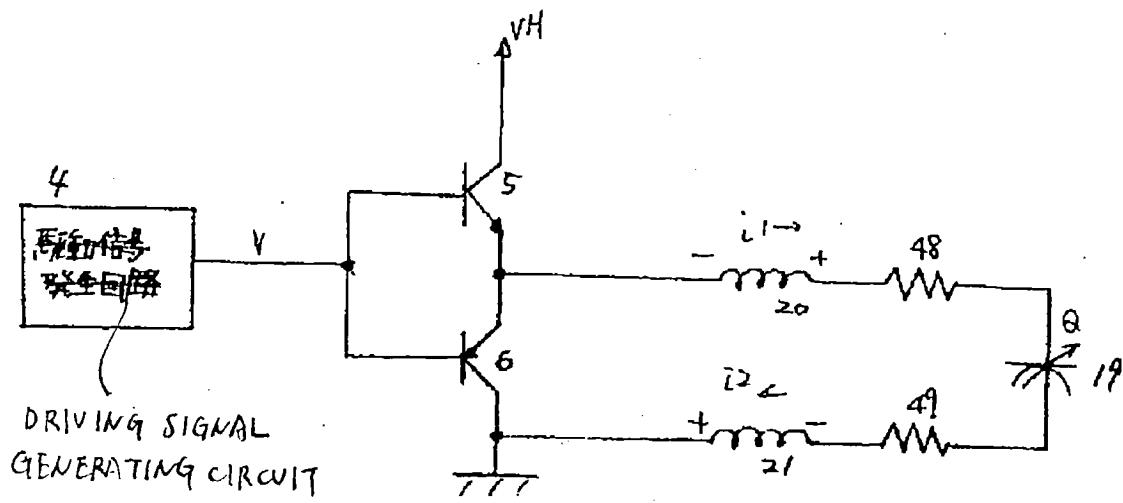


Fig. 4A

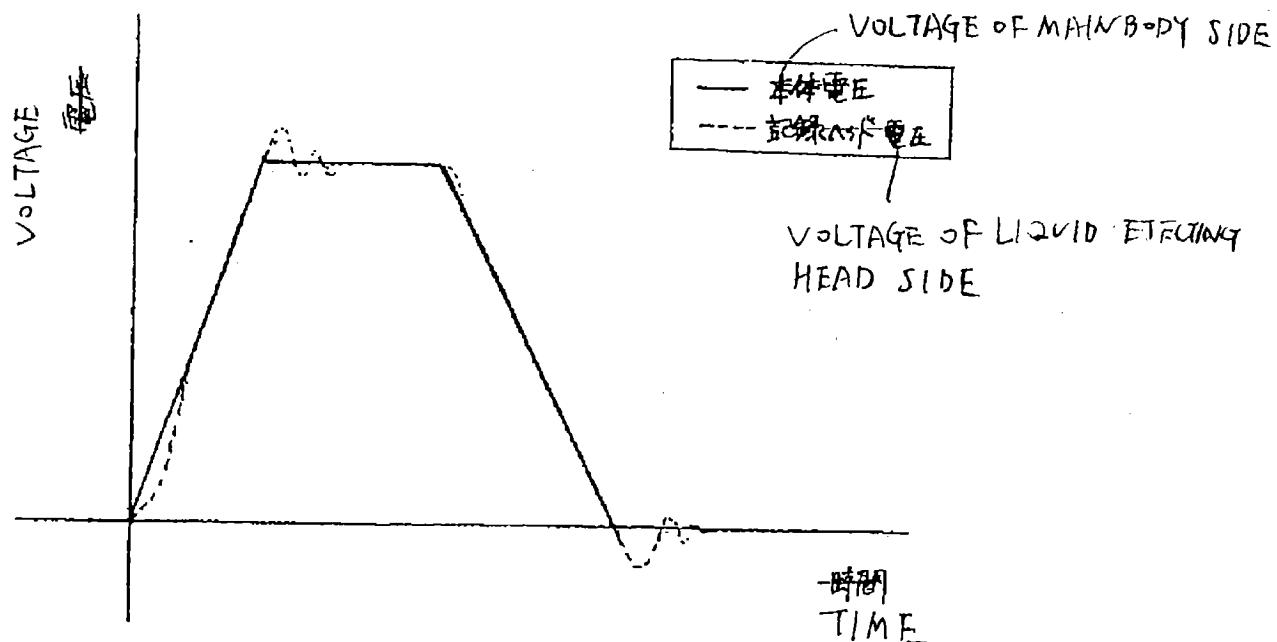


Fig. 4B

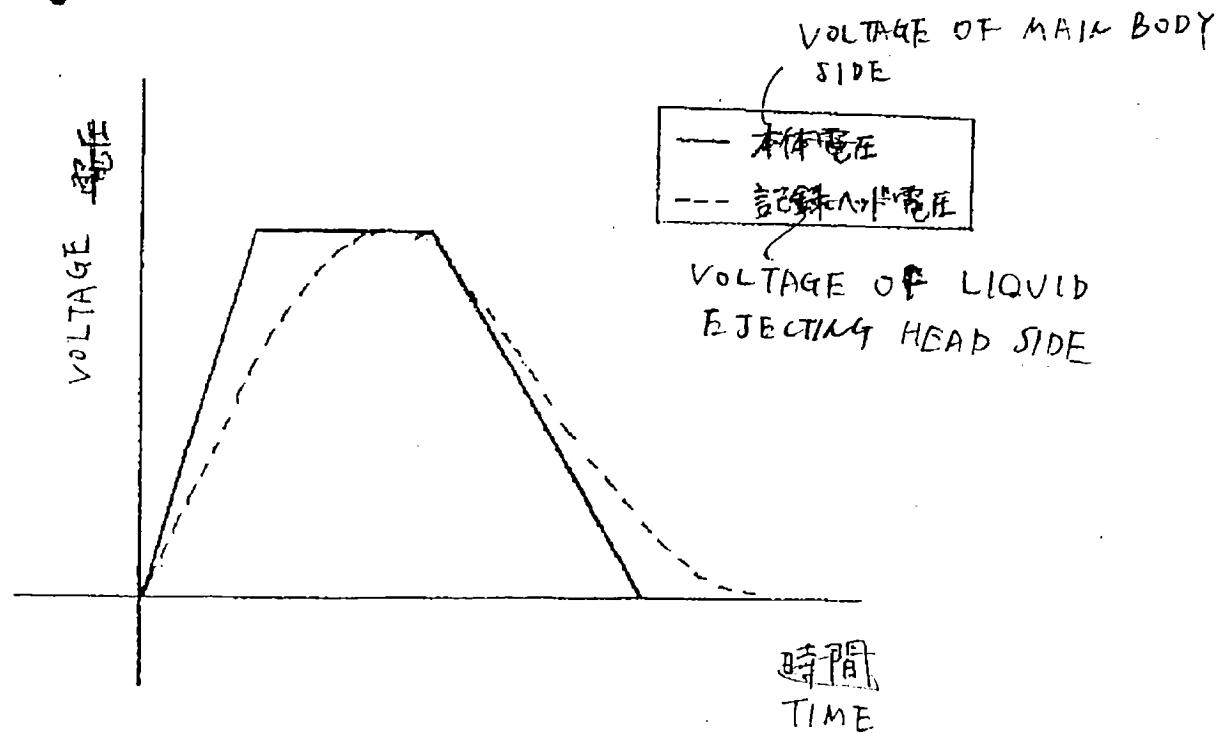


Fig. 5

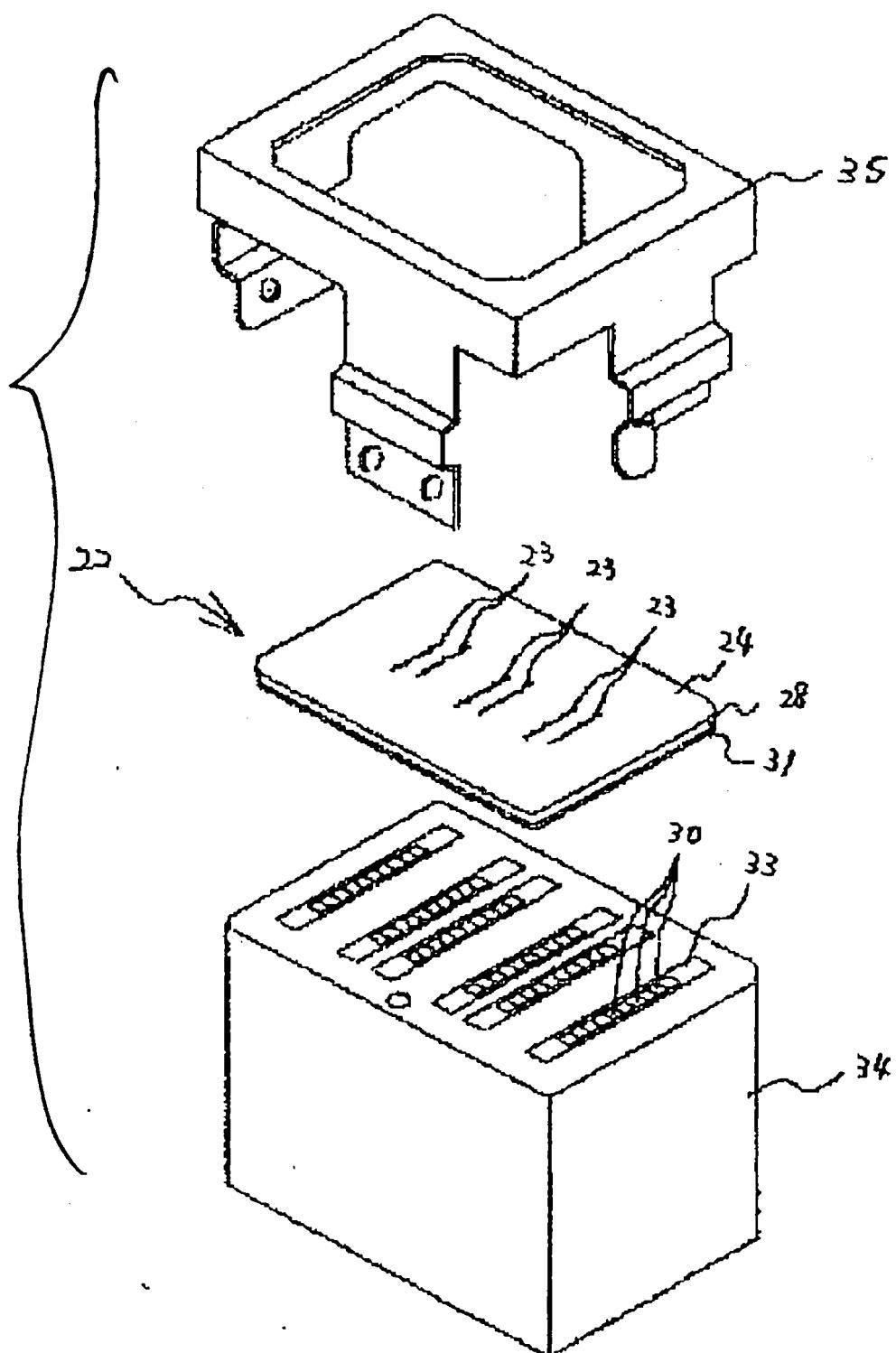


Fig. 6

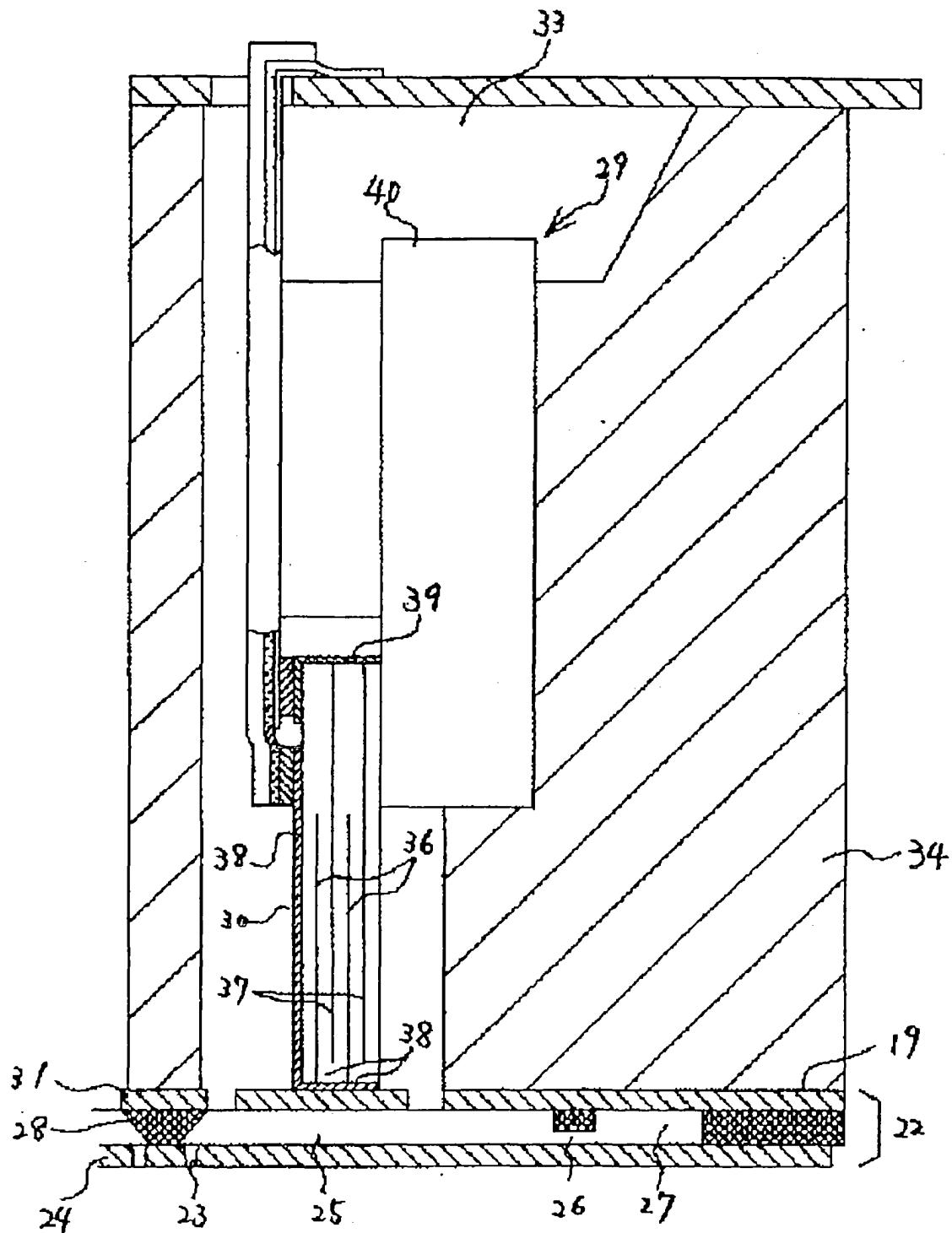


Fig. 7

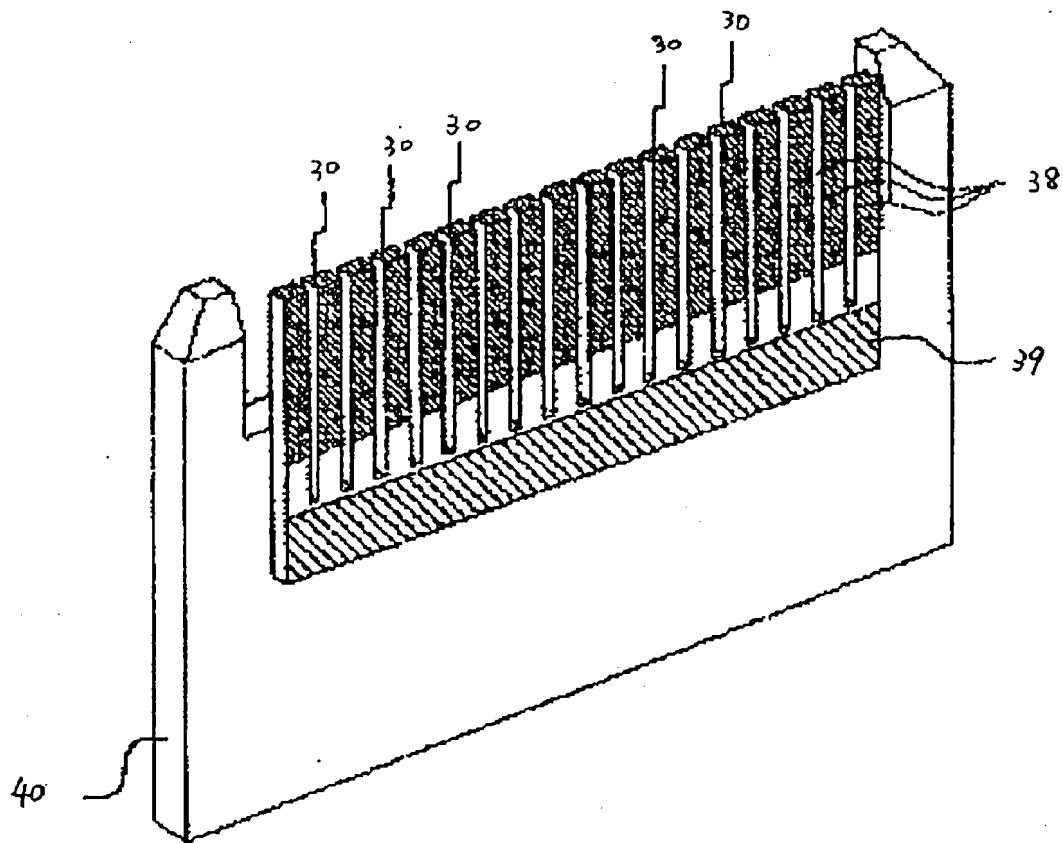


Fig. 8A

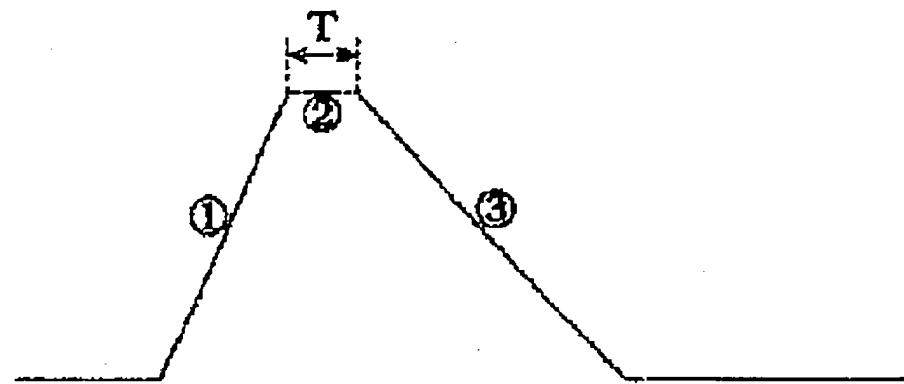


Fig. 8B

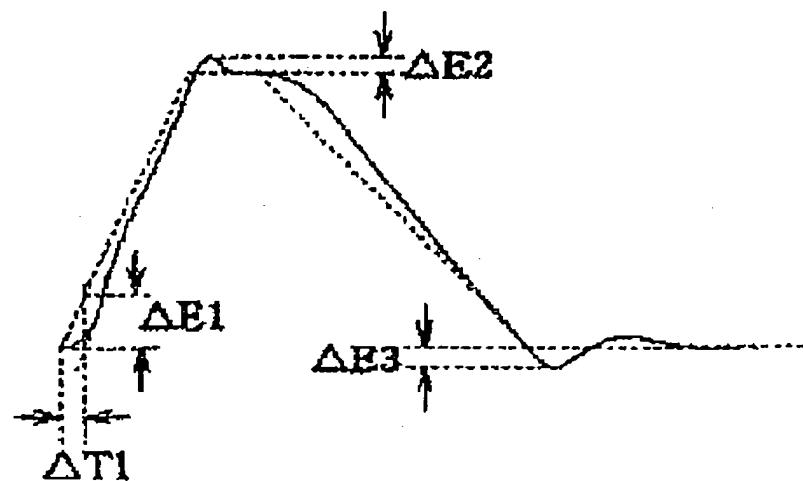


Fig. 9

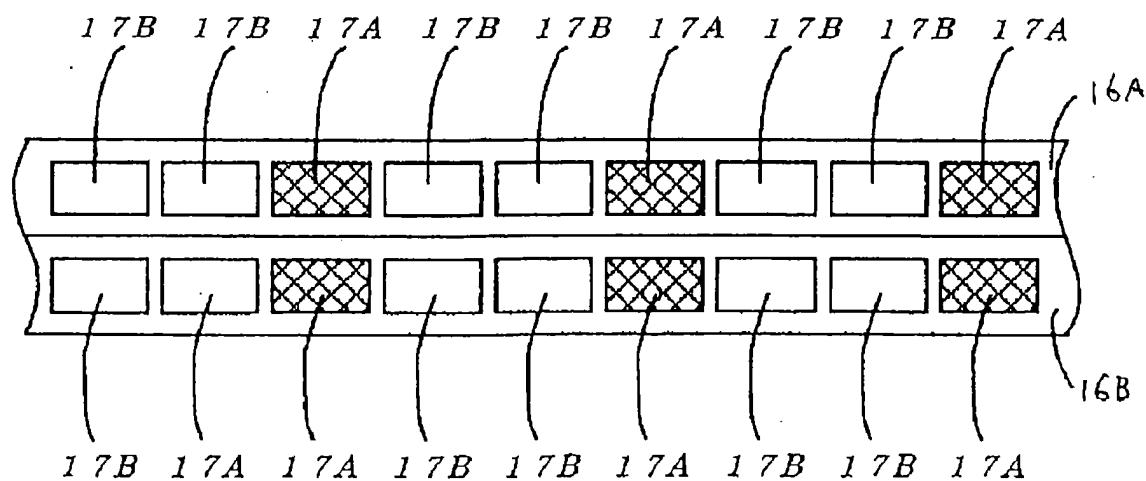


Fig. 10

